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# SN761643

SLES246-APRIL 2009

# AGC AMPLIFIER

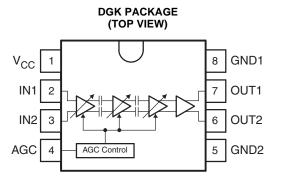
#### **FEATURES**

- Low-Distortion Automatic Gain Control (AGC)
  Amplifier
- 5-V Power Supply
- 8-Pin Mini Small-Outline Package (MSOP)
- Wide Gain Control Range

#### **APPLICATIONS**

- Digital TVs
- Digital CATVs
- Digital Set-Top Boxes (STBs)

#### DESCRIPTION



The SN761643 is an automatic gain control (AGC) amplifier for the TV tuner system of a digital TV, CATV, or STB. The circuit consists of three stages of controlled-gain amplification, followed by a fixed-gain output amplifier.

The device is packaged in an 8-pin MSOP suitable for surface mounting.



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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TEXAS INSTRUMENTS

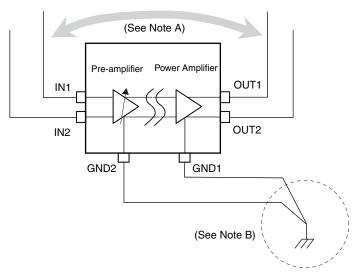
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#### **TERMINAL FUNCTIONS**

TERMINAL		I/O		DESCRIPTION		
NAME	NO.	1/0	EQUIVALENT CIRCUIT	DESCRIPTION		
AGC	4	I	4 60 Ω 100 kΩ 100 kΩ	Gain control voltage input		
GND1	8			Power amplifier ground		
GND2	5	-		Pre-amplifier ground		
IN1 IN2	2 3	I	$V_{\text{bias}}$ 2 $1 \text{ k}\Omega$ 2 $1 \text{ k}\Omega$ 3 $1 \text{ k}\Omega$ 3	AGC amplifier input		
OUT1 OUT2	7 6	0	15 Ω (7) (7)	AGC amplifier output		
V <sub>CC</sub>	1	_		5-V power supply		

#### **Correct Use**



- A. Be careful to keep enough isolation between input and output line.
- B. Form a ground pattern as widely as possible. GND1 and GND2 should not have common impedance.



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# ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
$V_{CC}$	Supply voltage range <sup>(2)</sup>	V <sub>CC</sub> (pin 1)	-0.4	6.5	V
VI	Input voltage range <sup>(2)</sup>	AGC (pin 4)	-0.4	V <sub>CC</sub>	V
$P_D$	Continuous total dissipation <sup>(3)</sup>			477	mW
$T_{JC}$	Maximum junction temperature			150	°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) Voltage values are with respect to the GND of the circuit.

(3) At  $T_A \leq 25^{\circ}$ C. For  $T_A > 25^{\circ}$ C, the derating factor is 3.82 mW/°C.

## **RECOMMENDED OPERATING CONDITIONS**

		MIN	NOM	MAX	UNIT
$V_{CC}$	Supply voltage	4.5	5	5.5	V
T <sub>OPE</sub>	Operating free-air temperature	-20		85	°C

#### DC ELECTRICAL CHARACTERISTICS

 $V_{CC} = 5 \text{ V}, T_A = 25^{\circ}\text{C}$  (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
I <sub>CC</sub>	Supply current	V <sub>AGC</sub> = 3 V		28		mA
I <sub>IAGC</sub>	Input current (AGC)	V <sub>AGC</sub> = 3 V		30	60	μA
V <sub>AGCMAX</sub>	AGC maximum gain control voltage	Maximum gain	3		$V_{CC}$	V
V <sub>AGCMIN</sub>	AGC minimum gain control voltage	Minimum gain	0		0.2	V

## **AC ELECTRICAL CHARACTERISTICS**

 $V_{CC}$  = 5 V,  $T_A$  = 25°C, parameters measured in test circuit (unless otherwise noted)

	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
G <sub>MAX1</sub>	Maximum gain 1	$V_{AGC} = 3 \text{ V}, f_{IN} = 44 \text{ MHz}, V_{IN} = -60 \text{ dBm},$ differential out, see Figure 10	57	61	65	dB
G <sub>MIN1</sub>	Minimum gain 1	$V_{AGC} = 0 V$ , $f_{IN} = 44 MHz$ , $V_{IN} = -60 dBm$ , differential out, see Figure 10	-7	-4	-1	dB
G <sub>MAX2</sub>	Maximum gain 2	$V_{AGC}$ = 3 V, $f_{IN}$ = 44 MHz, $V_{IN}$ = –60 dBm, see Figure 1 and Figure 11	51	55	59	dB
G <sub>MIN2</sub>	Minimum gain 2	$V_{AGC}$ = 0 V, $f_{IN}$ = 44 MHz, $V_{IN}$ = –60 dBm, see Figure 1 and Figure 11	-13	-10	-7	dB
GCR	Gain control range	$V_{AGC} = 0 V \text{ to } 3 V$		65		dB
V <sub>OUT</sub>	Output voltage	Single-ended output, see Figure 3		2.1		Vp-p
NF	Noise figure	Maximum gain, see Figure 2		11		dB
IM3	Third-order intermodulation distortion	$f_{\rm IN1}=43.5$ MHz, $f_{\rm IN2}=44.5$ MHz, Maximum gain, $V_{\rm OUT}=-2$ dBm/tone, 1Vp-p See Figure 5 and Figure 12		-50		dBc
IIP3	Input intercept point	Minimum gain		11		dBm
r <sub>IN</sub>	Input resistance (IN1, IN2)			1		kΩ
r <sub>OUT</sub>	Output resistance (OUT1, OUT2)			25		Ω

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**EXAS** 

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**TYPICAL CHARACTERISTICS** 35 60 50 30 40 25 30 Gain (dB) NF (dB) 20 20 10 15  $V_{CC} = 4.5 V$ 0  $V_{CC} = 5 V$ 10 -10  $V_{CC} = 5.5 V$ 5 -20 45 10 15 20 25 30 35 40 50 55 60 0.0 0.5 1.0 1.5 2.0 2.5 3.0 AGC (V) Gain (dB) Figure 1. Gain vs AGC (T<sub>A</sub> = 25°C) Figure 2. Noise Figure vs Gain ( $V_{CC} = 5 V$ ,  $T_A = 25^{\circ}C$ ) 30 60 20 50 10 0 Output Level (dBm) 40 -10 Gain (dB) -20 30 -30 -40 20 Maximum gain -50 Minimum gain -60 10 -70 -80 0 --70 -60 -50 -40 -30 -20 -10 0 10 10 100 Input Level (dBm) Frequency (MHz) Figure 3. Output Level vs Input Level (T<sub>A</sub> = 25°C) Figure 4. Gain vs Frequency (Gain = Max,  $T_A = 25^{\circ}C$ ) 10 0 -10 0 -20 -10 -30 Output Level (dBm) Output Level (dBm) -20 -40 42.5 MHz -50 -30 43.5 MHz -60 -40 -70 -50 -80 42.5 MHz -90 43.5 MHz -60 -100 -70 -110 -120--80 30 -35 -30 -25 -20 -15 -10 -5 Ó 0 20 40 10 50 60 5 10 15 Gain (dB) V<sub>IN</sub> (dBm) Figure 5. IM3 vs Gain ( $V_{CC} = 5 V$ ,  $T_A = 25^{\circ}C$ )

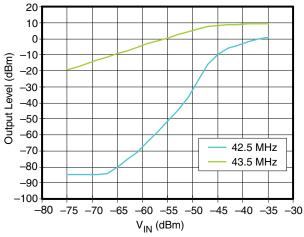


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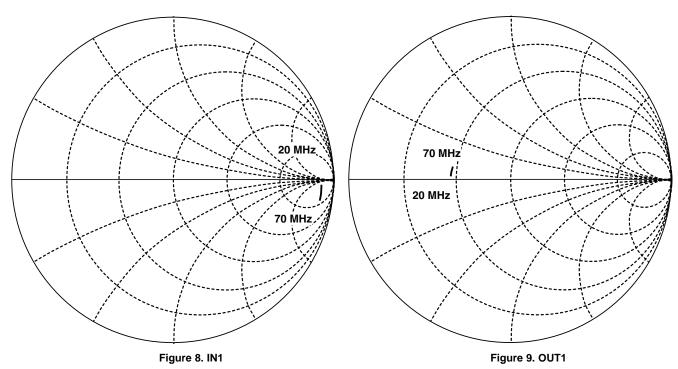
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## **TYPICAL CHARACTERISTICS (continued)**





#### **S-Parameter**



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## **APPLICATION INFORMATION**

### **Test Circuits**

This application information is advisory, and a performance check is required for actual application circuits.

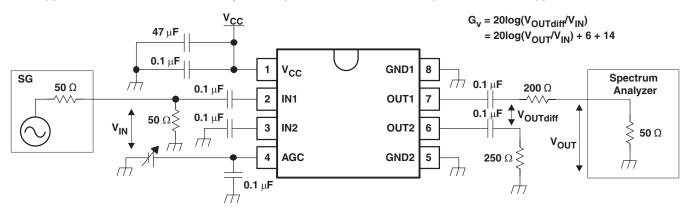


Figure 10. Measurement Circuit for Gain and Output Voltage 1

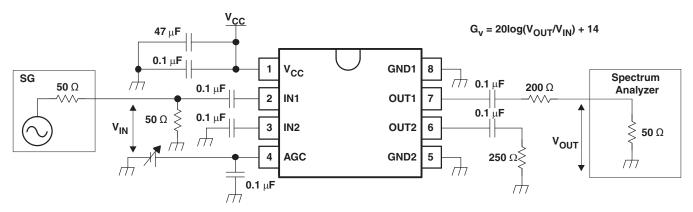


Figure 11. Measurement Circuit for Gain and Output Voltage 2

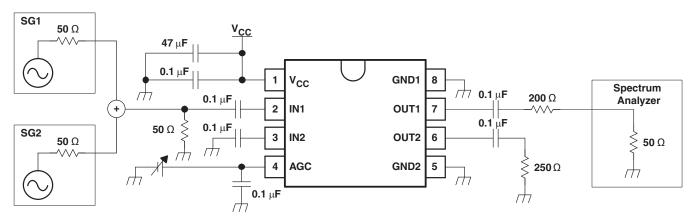


Figure 12. Measurement Circuit for IM3 and IIP3

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DGK (S-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 per end.

- D Body width does not include interlead flash. Interlead flash shall not exceed 0.50 per side.
- E. Falls within JEDEC MO-187 variation AA, except interlead flash.



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